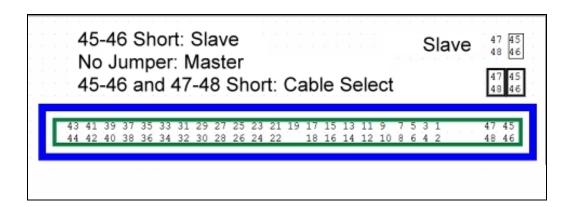
Master/Slave Jumper Select Instruction:



1. No Jumper: Master (default setting)

2. 45 - 46 Short: Slave

3. 45 - 46 and 47 - 48 Short: Cable Select



PhotoFast[®] G-Monster-1.8" IDE SSD Pin Assignment

Interface Pin Assignment, Signal Names and detail

Description:

obive inledface stenals						
Fin	SIENAL	FiN	STENAL			
1	a 252L	2	ABOUNO			
3	887	4	8 66			
5	886	6	8 86			
7	885	8	88 10			
9	884	10	88 11			
11	993	12	00 12			
13	882	14	00 13			
15	881	16	88 14			
17	88 0	18	00 1 5			
19	ABOUNO	20	KEY			
21	DMABQ	22	足 OUNO			
23	dion/stof	24	ABOUNO			
25	opob/ -Omaboy Hslbobs	26	対めらいの			
27	reoby-/kobamo-/kobog	28	ಗಜಾದ			
29	DMACH	30	KBOUND			
31	inlag	32	ĵoc 91 6			
33	0A1	34	FONE			
3 5	٥٨٥	36	0A2			
37	GSO	38	cg 1			
39	dagf	40	ABOUNO			
41	+5v	42	+5v			
43	KBOUNO	44	BZSZBVZQ			



Signal List:

g ignalnam e	FIN	3/0 type	Description
azszt.	1	î	this is a reset signat output from the host system and to be used for invertace logic circuit.
000-00 1 5	3-18	3/0	this is a 16-bitbi-directional data bus. the known 8 bits are used for register access other than data register.
ÒJOW	04	7	the rising edge of this write strate signal clocks data from the host data bus into a register on the device.
SLOF	23		Assertion of this signally the host during an use own burst signals the
୦ ୭୦୫		ĵ	Activating this bead state host alta pas the using eace of this signal bas the using eace of this
KOBANOH	2 5		this signalis a now controlsignalion und oma bead. Host assents this signal and indicates that the host is ready to receive und oma bead data.
HSLBOBS			this signalis white data stabe signalitam the hostion an ulta DMA white. Buth the rising and railing edge latch the data rum DD (15:0) into the device.
୯୦୫୦୪	27	O	this signalie used to temporally stop the host register access (read or
Kobamoh			this signalis a now controlsignal for used om a mite. Device asserts this signal and indicates that the device is ready to receive used om a write data.
Kobamoh			九元郎 STONALLTE the data in stable STONALLTOM the device for an ULKA OMA Bead. Both the ASING and Talling edge Idach the data From OO (15:0) inkothe host
тьва	31	O	this is an interrupt signal for the host system. This signal is asserted by a selected device when the nish bilin the device control is egister is "o". In other cases, this signal should be a high impedance state.

Signal List (Continued):

	70CS16	32	7/ 0	Exception DMA Adherers, incels. Indicates to the host system that the 16-bit. data porthas been addressed and that the drive is prepared to send or receive a 16-bit data word. This shall be an open collector output. - when adnerenting in fin Mode, indicessed, adnerers shall be 8-bit using DDO7. - when adnerenting in fin Mode, indicessed, adnerers shall be 16-bit using DDO-15. - when adnerenting in DMA Mode, the host shall use a 16-bit DMA channel. and incelse shall not seemed.
TOTALE TO COMPREND TO THILLDEVICES ON THE CHILDS: CSO- THE DEVICE CHIP SELECTION STANDILLE USED TO SELECTION COMMAINED ELECTION BEGINERER FORM THE HOCKLESTERM. THE BEGINER FORM THE HOCKLESTERM. THE STANDILLE CHIP SELECTION STANDILLE USED TO SELECTION COMMAINED USED THE COMMAINED CHIP SELECTION OF THE STANDILLE CHIP SELECTION STANDILLE USED TO SELECTION OF THE TURNED COMMAINED CHIP SELECTION WHEN THE STANDILLE CHIP ST	840-2	33,35,36	Î	this is a register address signal from the host system.
Registers from the host system. CS1- 38 7 Registers from the host system. Line device the selection standile used to select the control both Registers from the host system. Line standil herodes the state device it state when the power is turned on Upon receipt of command from the host the device sesents this standil Alcommand completion, the device de sesents this standil Alcommand completion, the device de sesents this standil Line device shall sesent this standil, used for own a day tangents bequeen host and device, when it is ready to tangent a day Line host in response to own as to either standilbuse this standil. Line device is configured as either device o (Massen) or device 1 (Signe) depending upon the standilbuse the standil - when Used as device o (Massen), cselic open - when used as device 1 (Signe), the host stall have pulliup resistor. Becommended pulliup resistor is lok ohm.	PONE	34	3/0	
Begreer from the hose state device is active when the power is turned on this signal indicates that a device is active when the power is turned on upon receivate command from the hose the device asserts this signal accommand from the hose the device asserts this signal accommand completion, the device de-asserts this signal. Like device shall assert this signal, used for DMA data tainsfers bequeen hose and device, when it is ready to tainsfer data. Like hose in response to DMABA to either ach now be attained has been accommed, or that data is a validable shall use this signal. Like device is configured as either Device o (Masser) or Device 1 (Signe) depending upon the signal bevelot 28 pin cerl signal. In hen used as Device 1 (Signe) the hose signal by the politic resistor. Becommended public resistor is 10% ohm.	G80-	37	ĵ	
DASF- 39 7/0 Upon receiptor a command from the host, the device asserts this signal. Atcommand completion, the device de asserts this signal. Line device shall assert this signal, used for DMA data tansfers bequeen host and device, when it is ready to tansfer data. DMACK 29 7 the host in response to DMAB a to either achnowledge that data has been accepted, or that data is available shall use this signal. The device is configured as either Device o (Master) or Device 1 (SIAVE) depending upon the signal evel 28 pin csel signal. — when used as Device 1 (SIAVE), the host shall have pulled resistor. Becommended pulled resistor is 10km ohm.	C31 -	38	ĵ	
DMACH 29 3 Line host in response to DMAB a to either achnowledge that data has been accepted, or that data is available shall use this signal. Line device is configured as either Device o (Master) or Device 1 (Sizive) depending upon the signal below of the notice 1 (Sizive), the host shall have pulliup resistor. Becommended pulliup resistor is 10% ohm. 2, 19, 22,24, 26, 30, 40, 43	dasf-	39	9/0	this signalinalisates that a device is active when the power is turned on. Upon receiptor a command from the host the device asserts this signal Atommand completion, the device de-asserts this signal
been accepted, or that data is available shall use this signal. the device is configured as either device o (Master) or device 1 (Stave) depending upon the signal evel us 28 pin cszl signal - w hen used as device o (Master), cszl is open - w hen used as device 1 (Stave), the host shall have pulliup resistor. Becommended pulliup resistor is 10% ohm. Eround 2, 19, 22,24, 26, 30, 40, 43	∂ MABQ	21	O	
(SERVE) depending upon the signal levelof 28 pin cszl signal - w hen used as device o (Maskh), cszl is open - w hen used as device 1 (SERVE), the hoselshall have pullup resistor. Becommended pullup resistor is 10% ohm. 2, 19, 22,24, 26, 30, 40, 43	DMACH	29	ĵ	
26, 30, 40, 43 Fin	ರಕಪಗಿ	28	7	(Stave) depending upon the signallevelof 28 pin cerl signal - when used as device 1 (Stave), the hostshall have pullup resistor. Becommended pullup
Fower 41,42 F 5v power Fin	eround		p	eround fin
	Fower	41, 42	p	5v powertin

Dimension:

